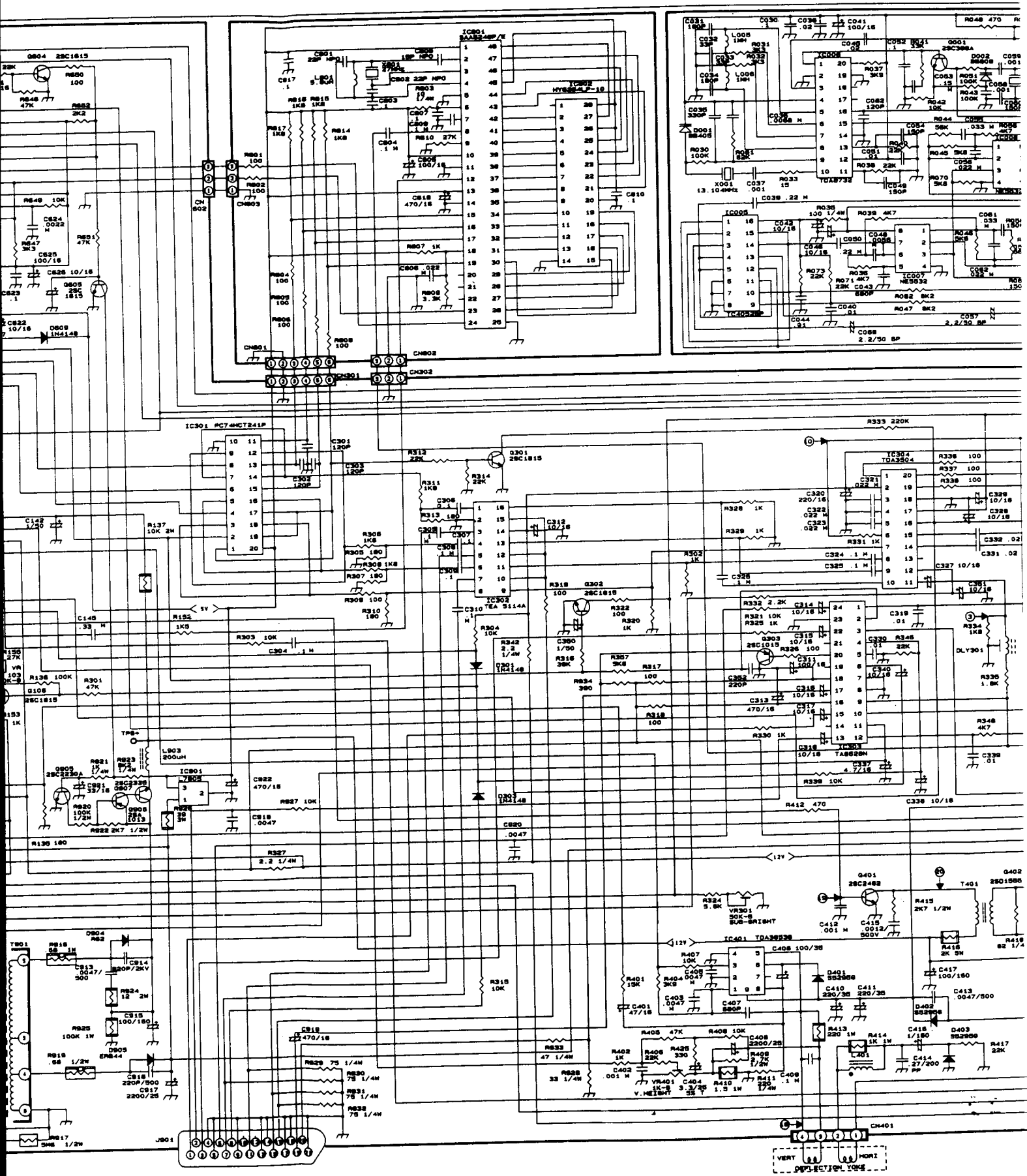
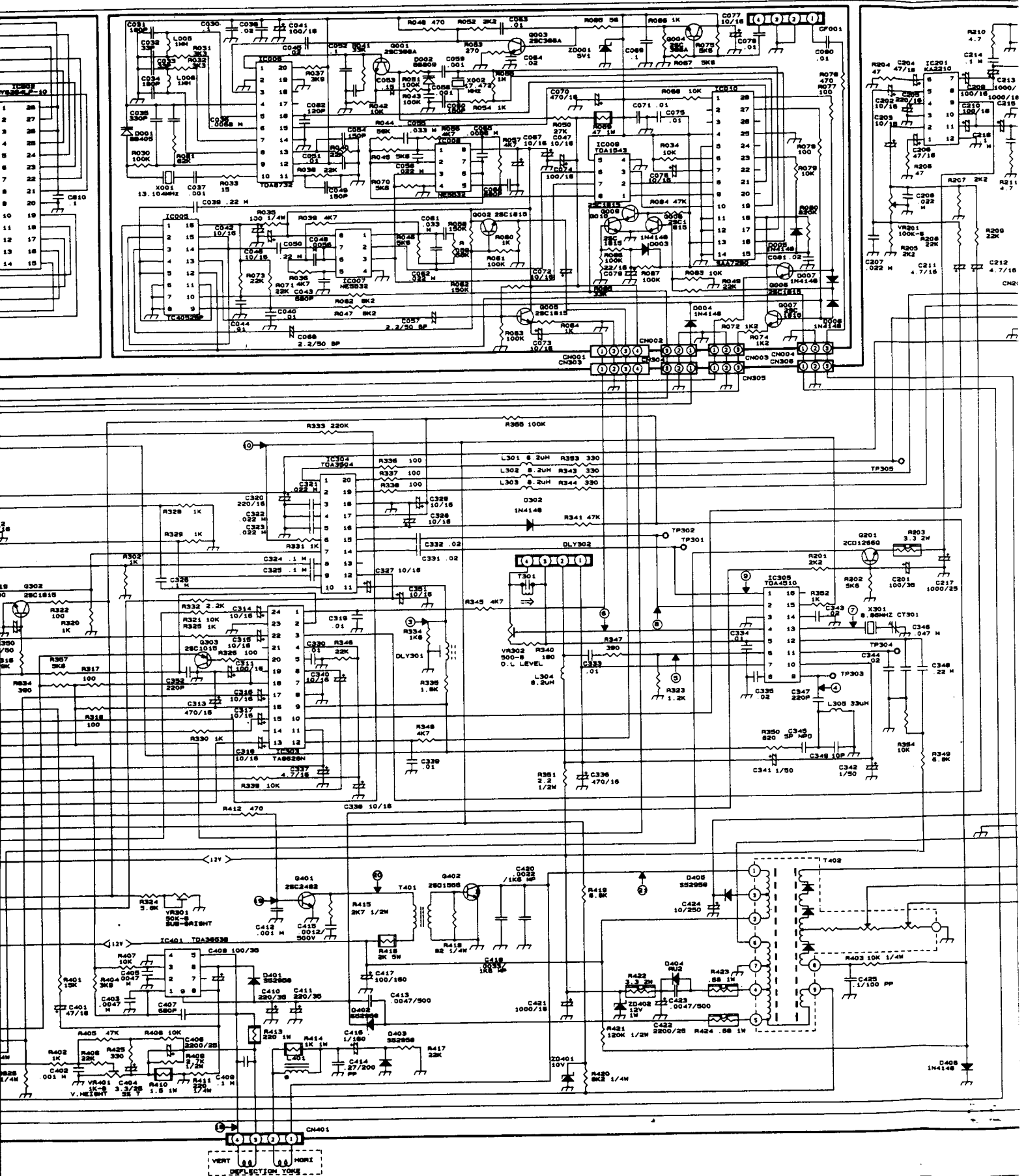
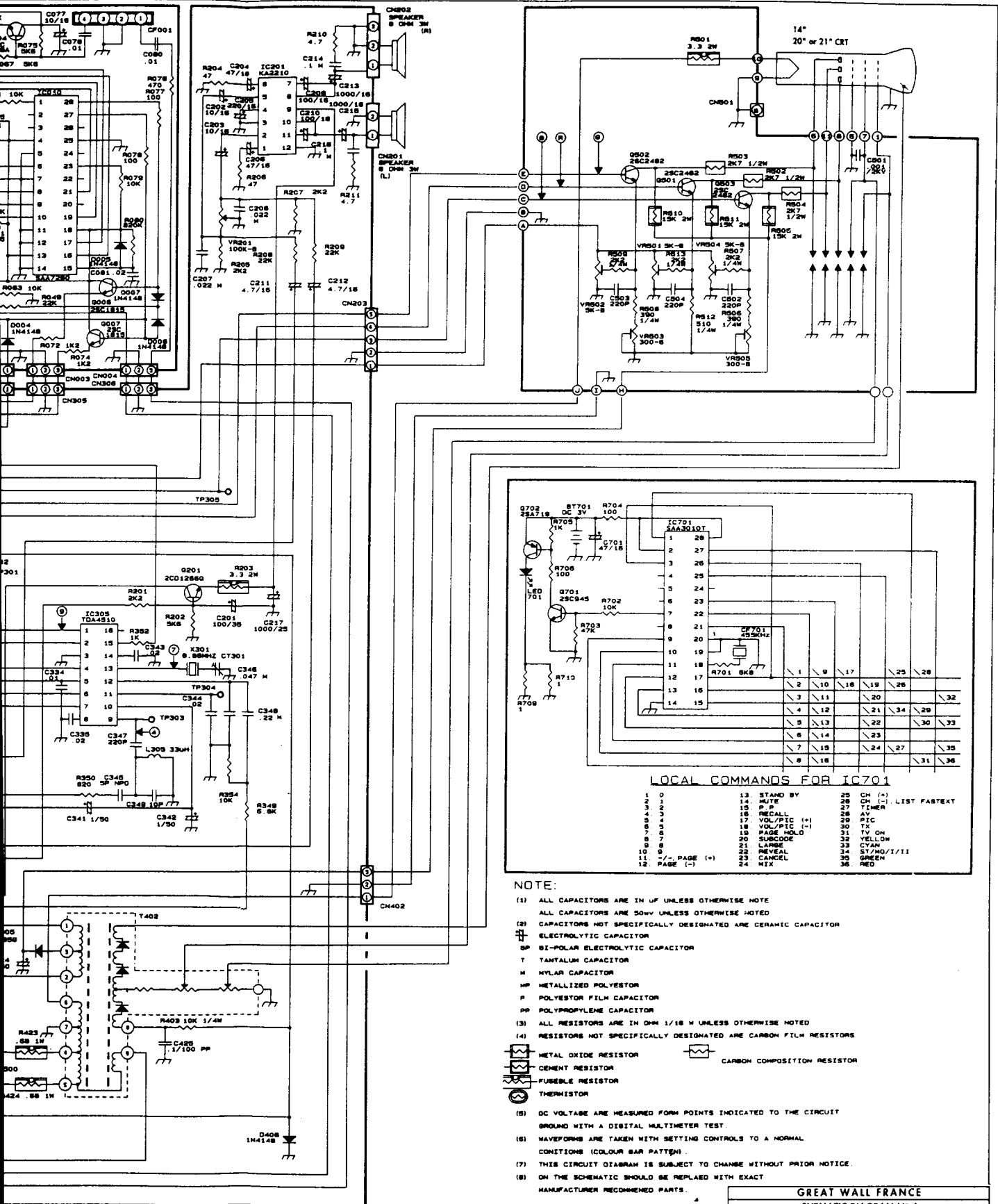


LOCAL COMMANDS

S801	PT (+)	S808	PIC (+)
S802	PT (-)	S809	PIC (-)
S803	AS	S810	CH (+)
S804	AV	S811	CH (-)
S805	PIC	S813	VOL (+)
S806	STORE	S814	VOL (-)







LOCAL COMMANDS FOR IC701

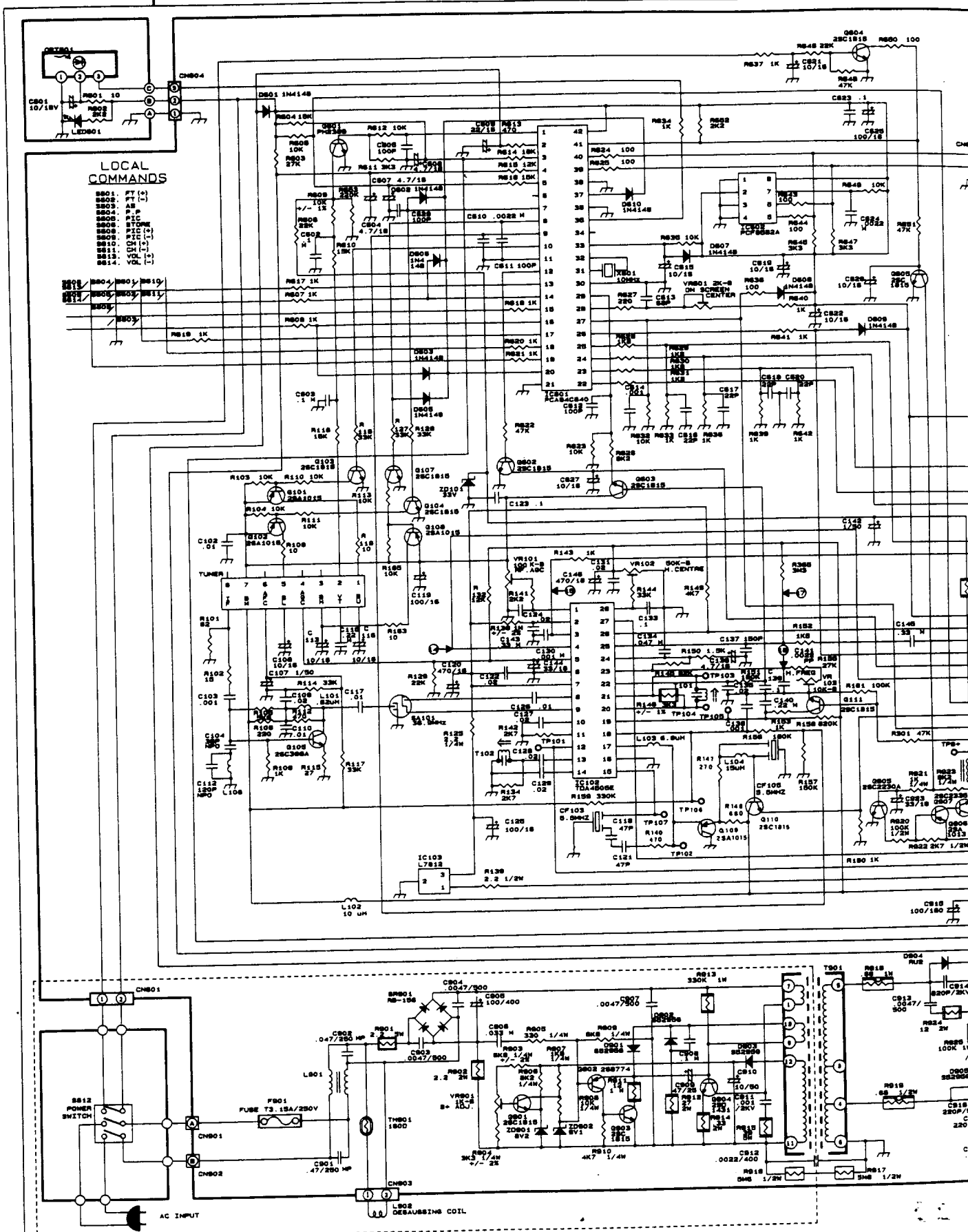
1	0	13	STAND BY	25	CH (+)
2	1	14	MUTE	26	CH (-) LIST FASTEXT
3	2	15	P.P	27	TIMER
4	3	16	RECALL	28	AV
5	4	17	VOL/PIC (+)	29	PIC
6	5	18	VOL/PIC (-)	30	TK
7	6	19	PAGE HOLD	31	TV ON
8	7	20	SURCODE	32	YELLOW
9	8	21	LARGE	33	CYAN
10	9	22	REVEAL	34	SYM/T/1/11
11	0/-	23	CANCEL	35	GREEN
12	PAGE (-)	24	MIX	36	RED

NOTE:

- (1) ALL CAPACITORS ARE IN μ F UNLESS OTHERWISE NOTED
ALL CAPACITORS ARE 50V UNLESS OTHERWISE NOTED
- (2) CAPACITORS NOT SPECIFICALLY DESIGNATED ARE CERAMIC CAPACITOR
ELECTROLYTIC CAPACITOR
BP BI-POLAR ELECTROLYTIC CAPACITOR
T TANTALUM CAPACITOR
M NYLAR CAPACITOR
MP METALLIZED POLYESTER
P POLYESTER FILM CAPACITOR
PP POLYPROPYLENE CAPACITOR
- (3) ALL RESISTORS ARE IN OHM 1/16 W UNLESS OTHERWISE NOTED
- (4) RESISTORS NOT SPECIFICALLY DESIGNATED ARE CARBON FILM RESISTORS
METAL OXIDE RESISTOR
CEMENT RESISTOR
FUSIBLE RESISTOR
THERMISTOR
CARBON COMPOSITION RESISTOR
- (5) DC VOLTAGE ARE MEASURED FROM POINTS INDICATED TO THE CIRCUIT GROUND WITH A DIGITAL MULTIMETER TEST.
- (6) WAVEFORMS ARE TAKEN WITH SETTING CONTROLS TO A NORMAL CONDITIONS (COLOUR BAR PATTERN).
- (7) THIS CIRCUIT DIAGRAM IS SUBJECT TO CHANGE WITHOUT PRIOR NOTICE.
- (8) ON THE SCHEMATIC SHOULD BE REPLACED WITH EXACT MANUFACTURER RECOMMENDED PARTS.

GREAT WALL FRANCE
SCHEMATIC DIAGRAM N° 4

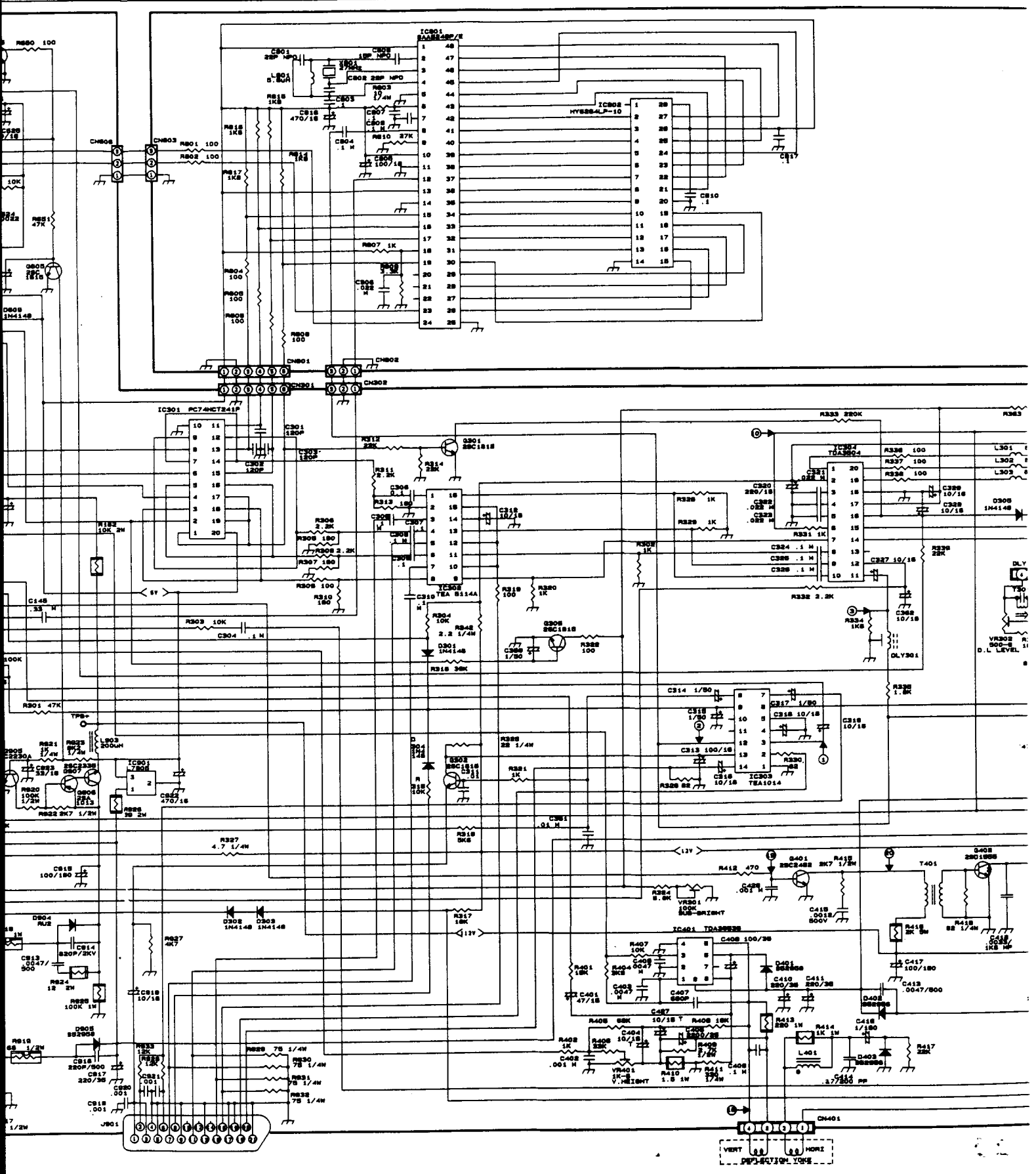
PAL I WITH S. TEXT NICAM SCART PLUG	SUITABLE FOR MODELS 8920 B 9220 8821 - 8921
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LOCAL COMMANDS

- S801. FT (+)
- S802. FT (-)
- S803. AB
- S804. P.P
- S805. BTONE
- S806. PIC (+)
- S807. PIC (-)
- S808. CH (+)
- S809. CH (-)
- S810. VOL (+)
- S811. VOL (-)
- S812. VOL (0)

IC801 SA8224B				IC801 PC84C840P				IC102 T04490S			
VDD	1	48	WE	VTUNN	1	42	VDD	1	28	PHASE 2	X7
OBCOUT	2	47	OE	VOL	2	41	STOBY	2	27	DEP	59H
OBCIN	3	46	A12	BAL	3	40	30A	3	26	SANDCASTLE OUTPUT/ FLYBACK INPUT	20
OCSBND	4	45	A13	BAT	4	39	SCL	4	25	HGR. DRIVE	21
VSSA	5	44	A10	CON/HUE	5	38	SYSTEM	5	24	SYNC SEPARATION	22
REF+	6	43	A8	BAL/TON/HUE	6	37	EFFECT	6	23	PHASE 1 DET.	23
BLACK	7	42	A8	VMP-L	7	36	SNOO	7	22	HGR. OSC.	NO DATA
										COINCIDENCE DET. DECOUPLING	



IC701 SAA3010

x7	1	28	V _{DD}
38H	2	27	X6
20	3	26	X5
21	4	25	X4
22	5	24	X3
23	6	23	X2
NO DATA	7	22	X1

IC802 HY8284LP

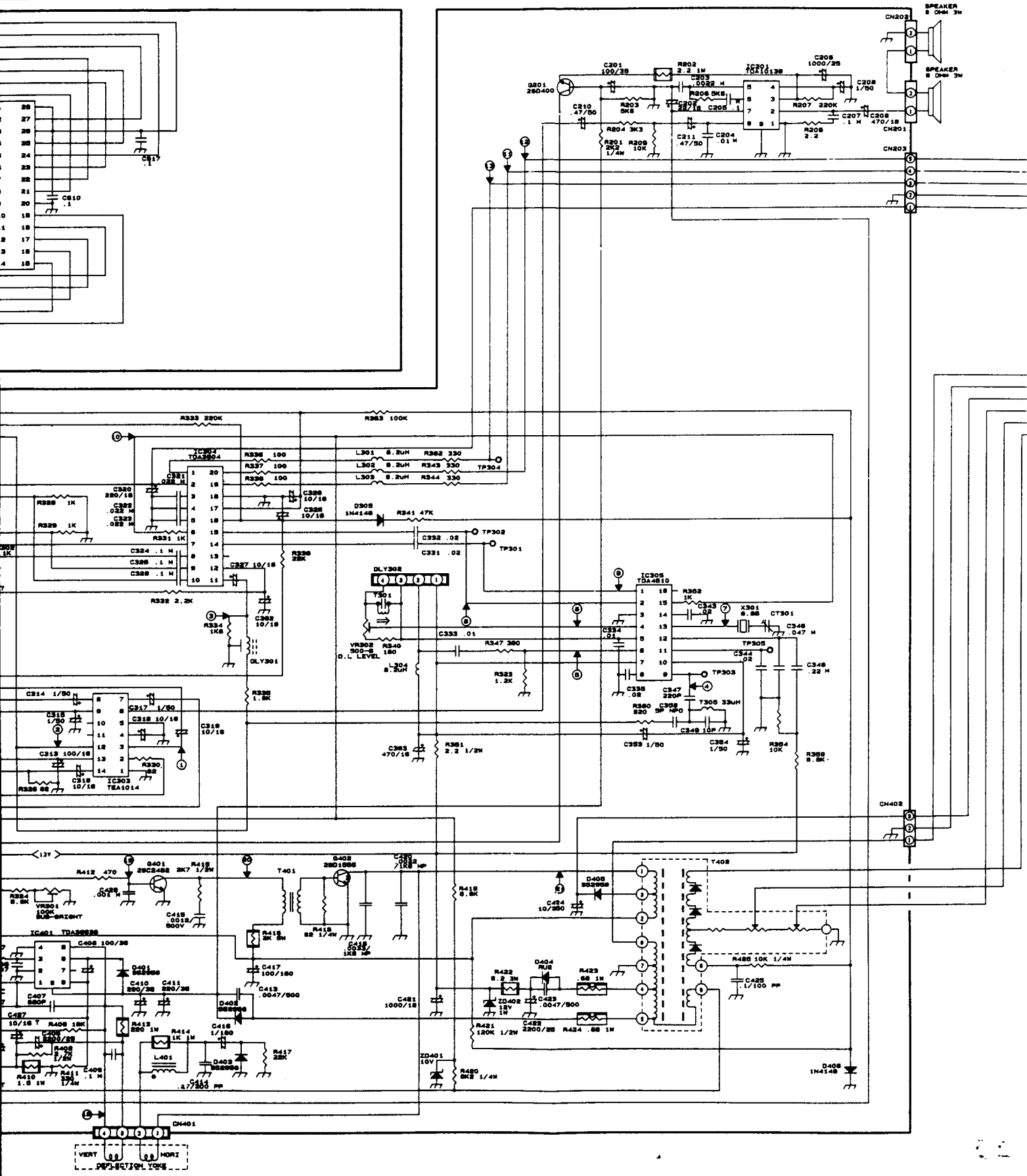
NC	1	28	V _{CC}
A12	2	27	WE
A7	3	26	CBE
A8	4	25	AB
A8	5	24	AB
A4	6	23	A11
A3	7	22	CE

IC804 TA38804

BLUE OUTPUT	1	30	GREEN OUTPUT
POSITIVE SUPPLY VOLT. (+15V)	2	19	RED OUTPUT
BLUE STORAGE FOR BRI.	3	18	END (0 V)
GREEN STORAGE FOR BRI.	4	17	BRI. CONTROL INPUT
RED STORAGE FOR BRI.	5	16	CONTRAST CONTROL INPUT
SANDCASTLE PULSE INPUT	6	15	COLOR DIFFERENCE INPUT (-B-V)
FAST SWITCH FOR RGB TEMP.	7	14	COLOR DIFFERENCE

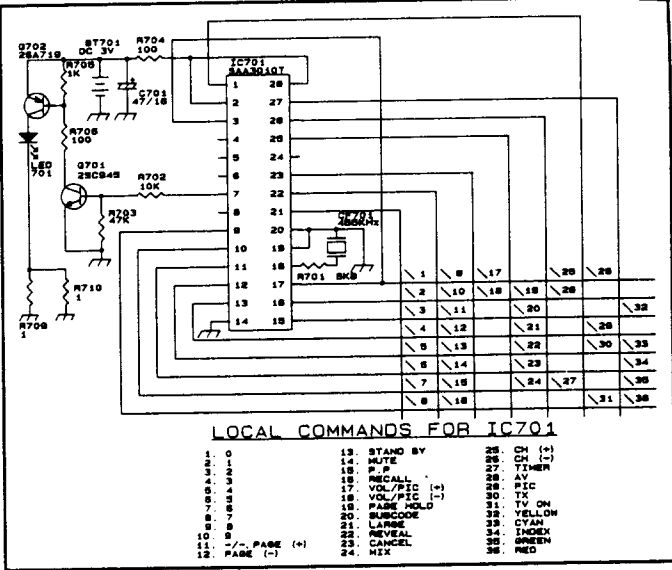
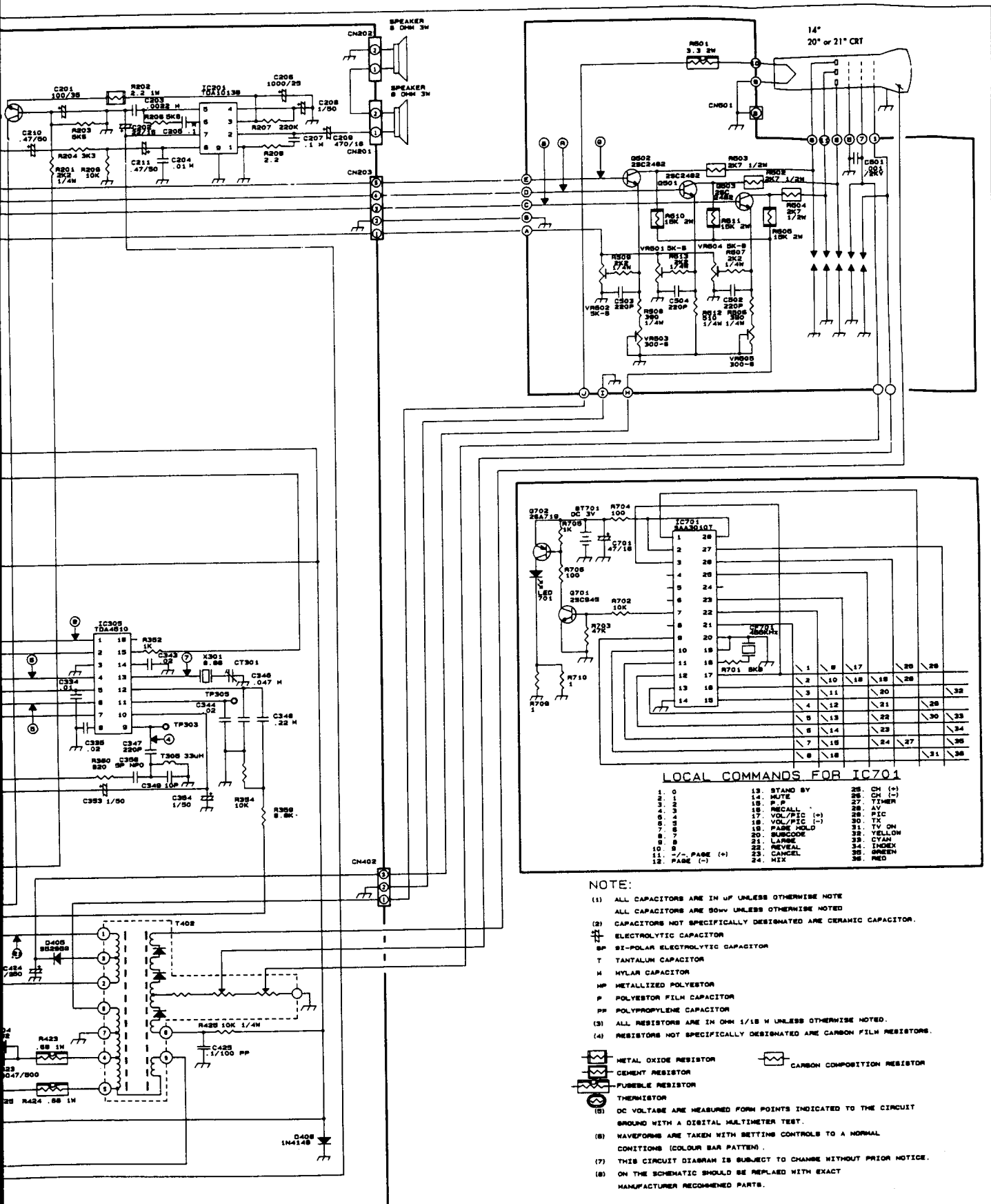
IC801 PC74NCT241P

10E	1	20	V _{CC}
1A0	2	18	80E
8Y0	3	18	1Y0
1A5	4	17	2A0
2Y1	5	18	1Y1
1A2	6	18	2A1
2Y2	7	14	1Y2



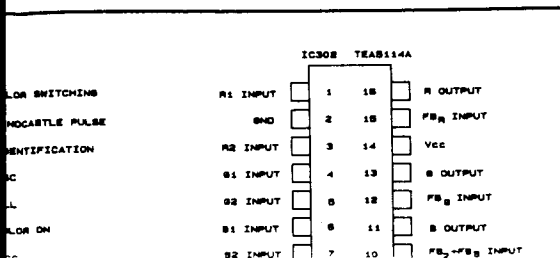
IC901 PC74MCT24P				IC908 TEA814A				
BRIGHT OUTPUT	10E	1	20	Vcc	R1 INPUT	1	16	R OUTPUT
RED OUTPUT	1A0	2	16	80E	8ND	2	16	FB _A INPUT
8ND (0 V)	8Y0	3	16	1Y0	R2 INPUT	3	14	Vcc
BRI. CONTROL INPUT	1A1	4	17	2A0	81 INPUT	4	13	8 OUTPUT
CONTRAST CONTROL INPUT	2Y1	5	16	1Y1	82 INPUT	5	12	FB _B INPUT
COLOR DIFFERENCE INPUT-(B-Y)	1A2	6	18	2A1	81 INPUT	6	11	8 OUTPUT
COLOR DIFFERENCE INPUT-(R-Y)	2Y2	7	14	1Y2	82 INPUT	7	10	FB ₂ -FB ₃ INPUT

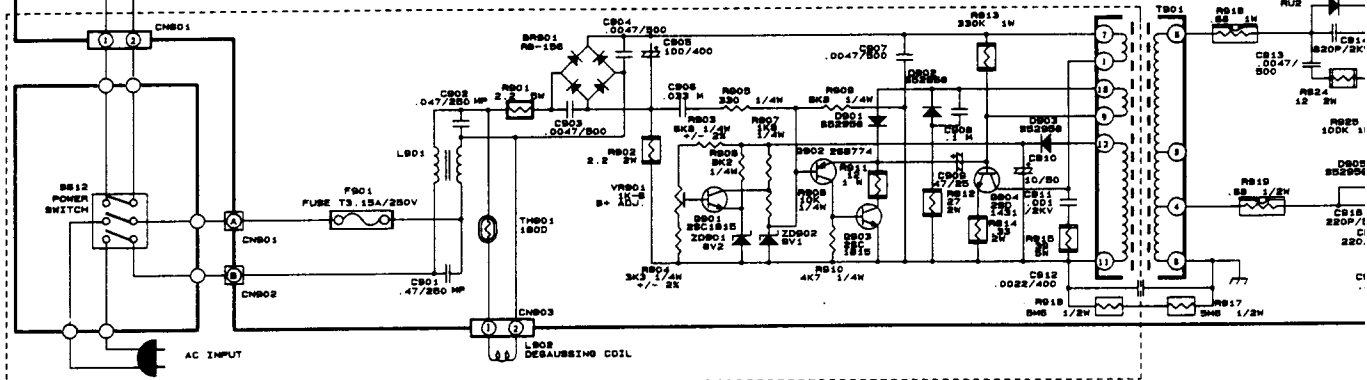
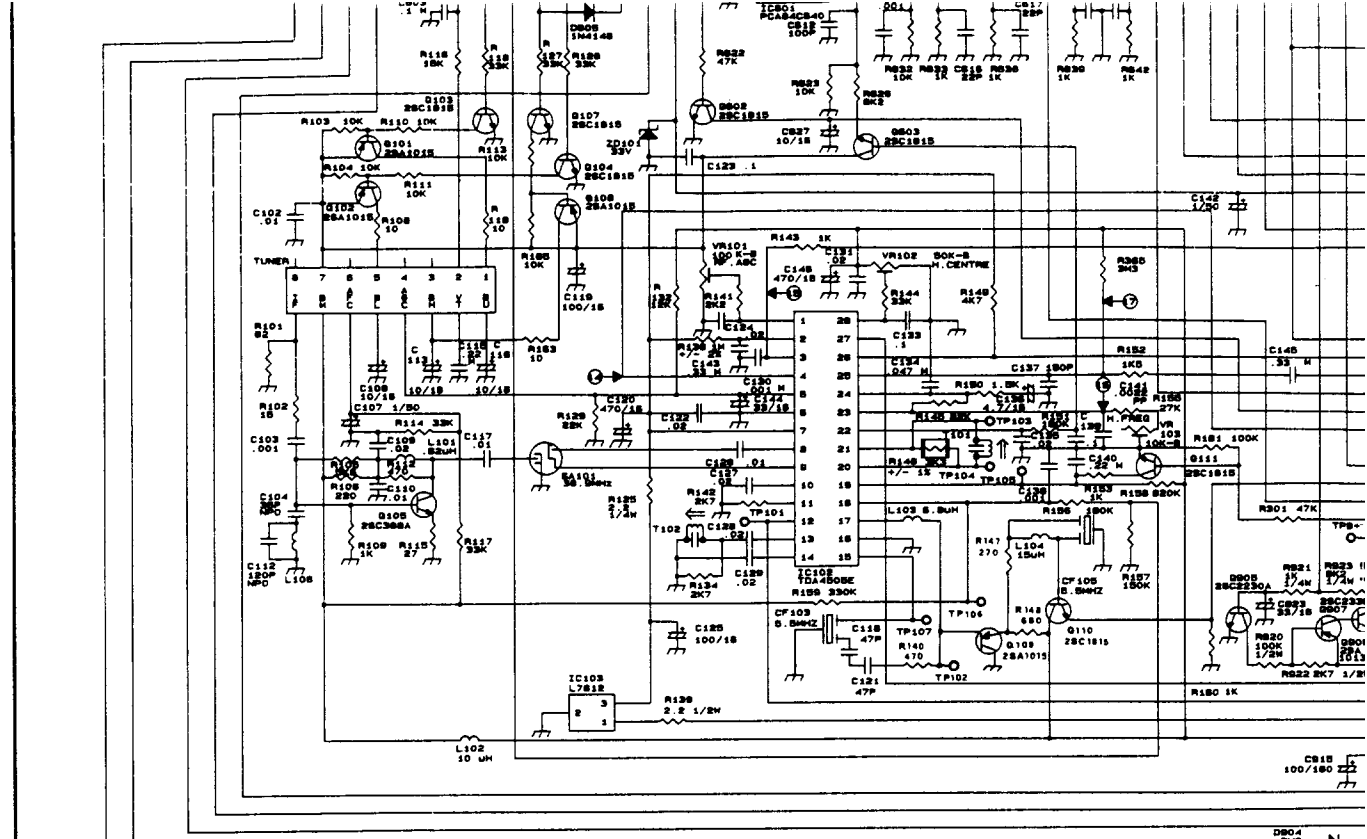
IC905 TDA4810			
-(R-Y)	1	18	COLOR SWITCHING
-(B-Y)	2	18	SANDCASTLE PULSE
8ND	3	14	IDENTIFICATION
DELAY LINE IN	4	13	08C
DC	5	12	PLL
DELAY LINE OUT	6	11	COLOR ON
VP	7	10	ACC



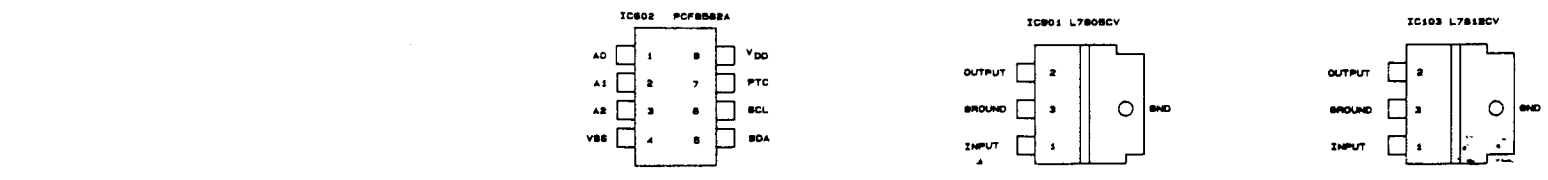
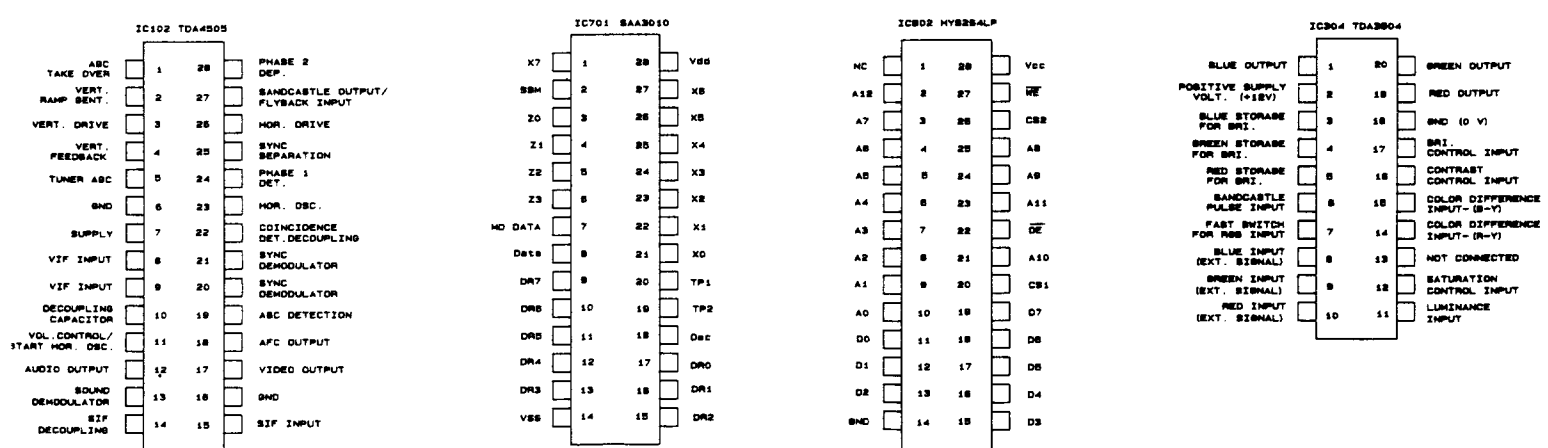
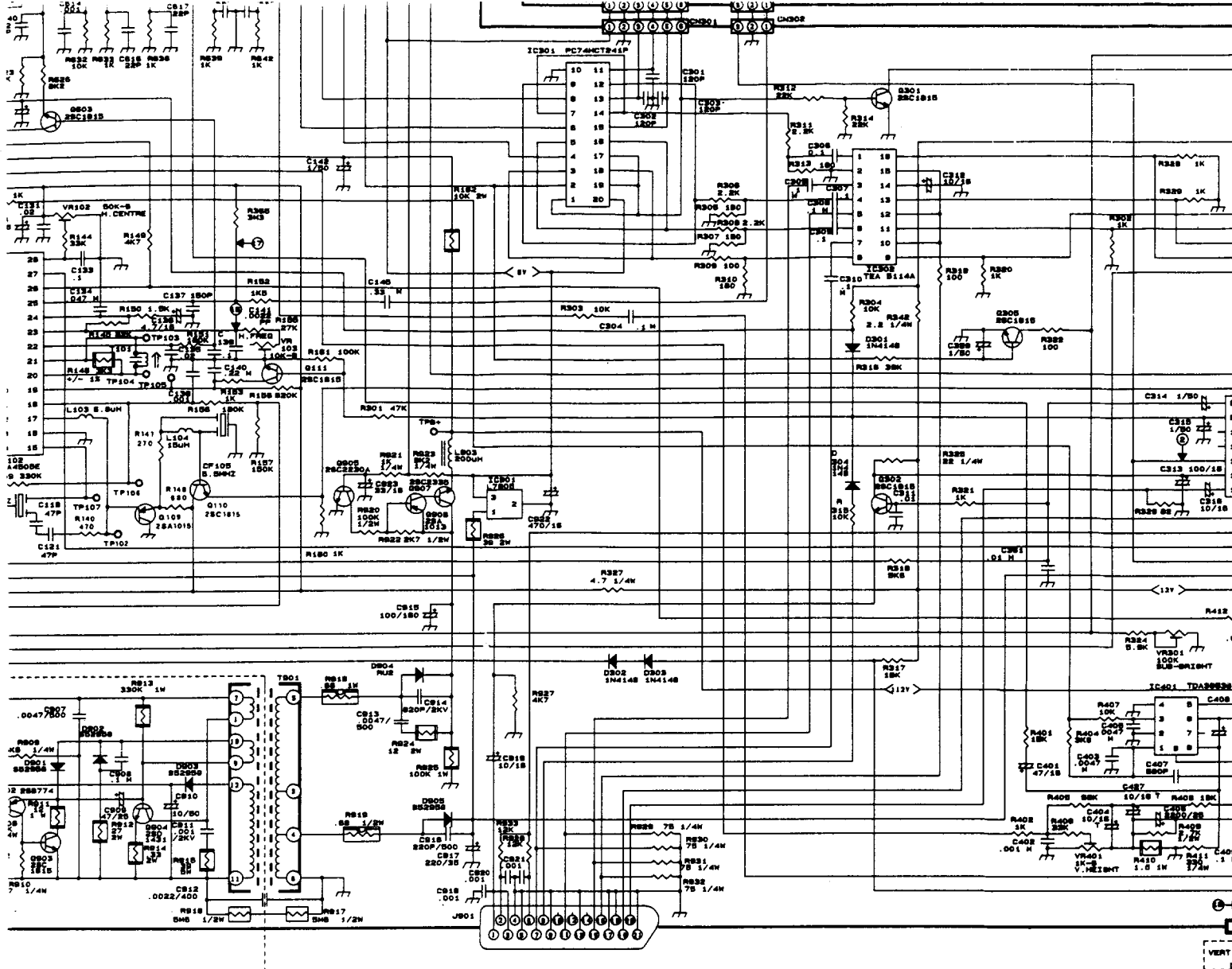
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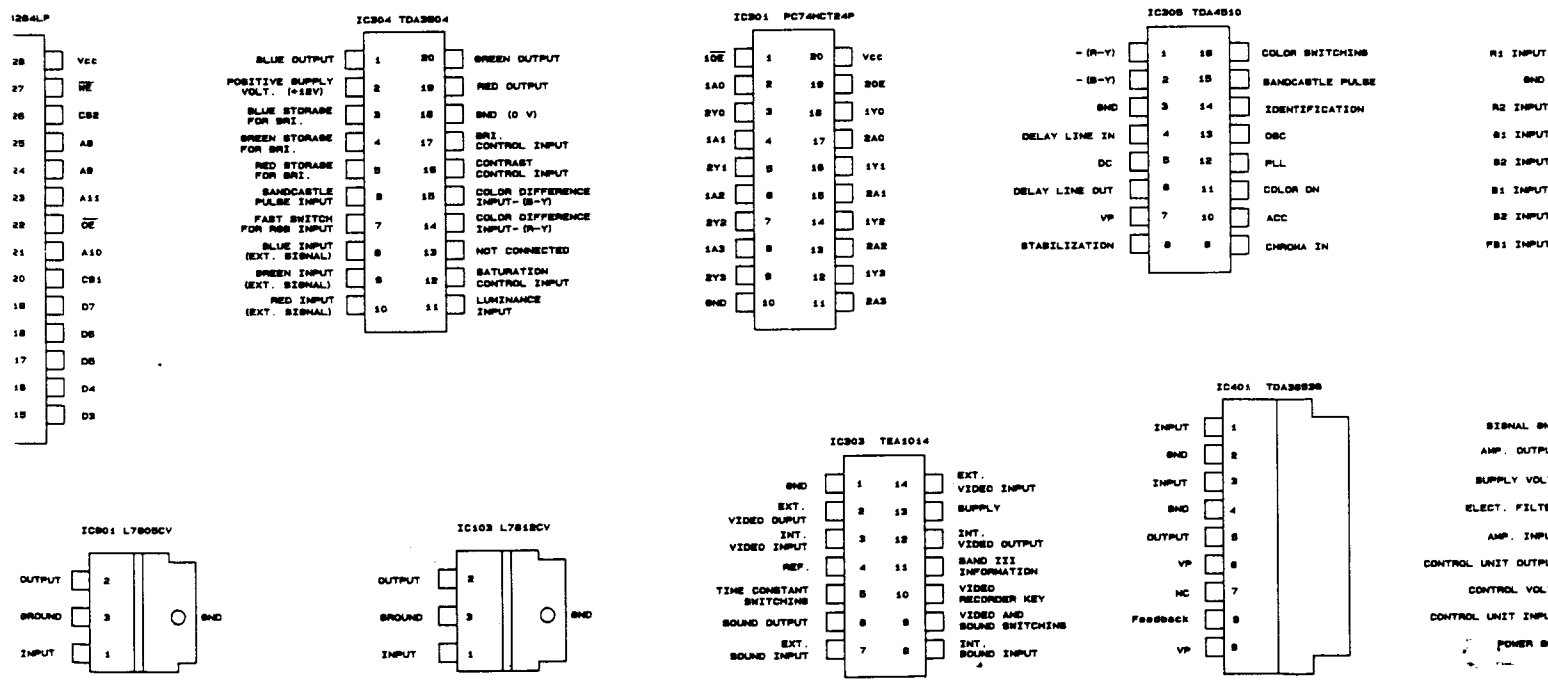
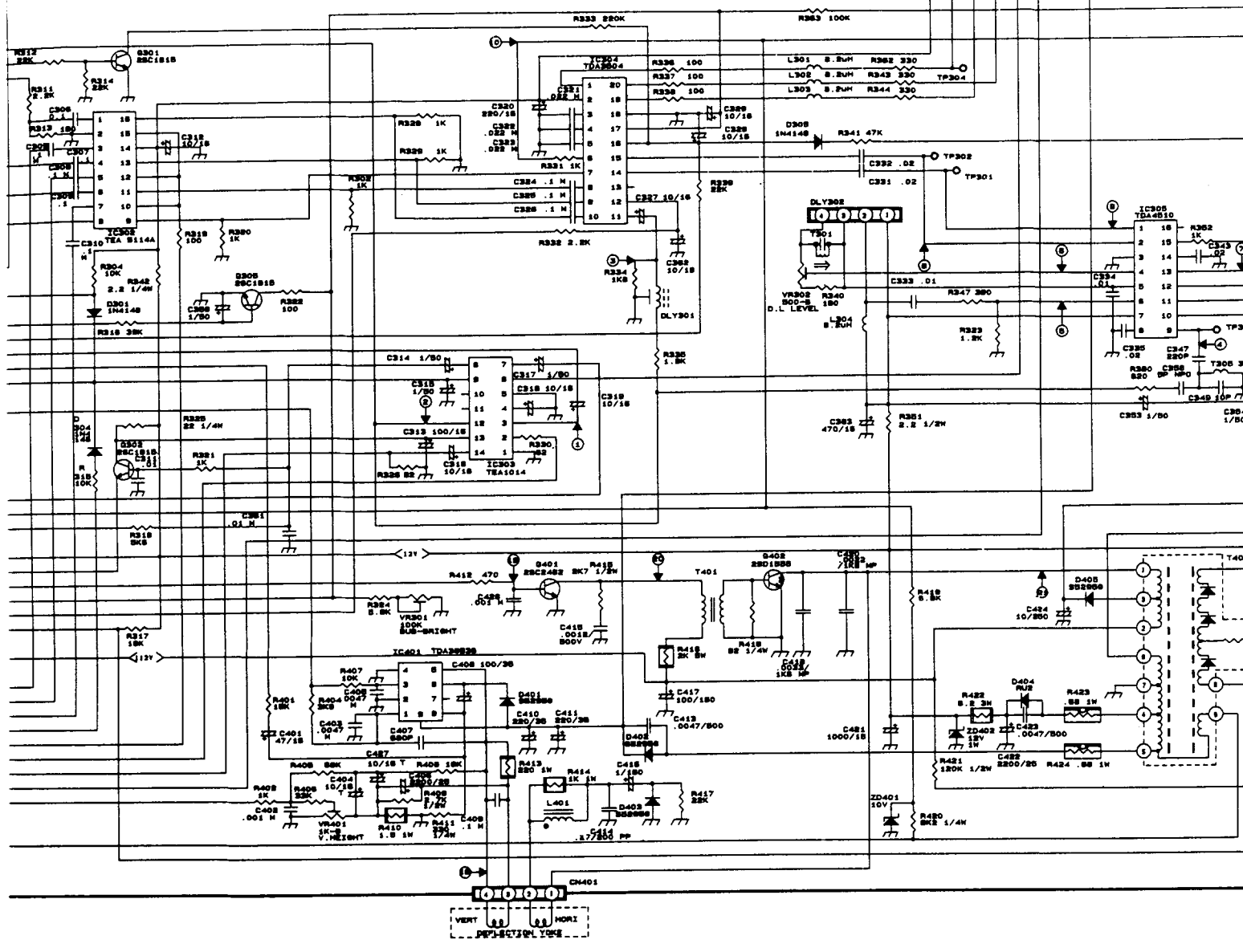
- (1) ALL CAPACITORS ARE IN UF UNLESS OTHERWISE NOTE
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 - ⊘ ELECTROLYTIC CAPACITOR
 - ⊖ SP BI-POLAR ELECTROLYTIC CAPACITOR
 - T TANTALUM CAPACITOR
 - M MYLAR CAPACITOR
 - MP METALLIZED POLYESTER
 - P POLYESTER FILM CAPACITOR
 - PP POLYPROPYLENE CAPACITOR
- (3) ALL RESISTORS ARE IN OHM 1/16 W UNLESS OTHERWISE NOTED.
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 - ⊘ METAL OXIDE RESISTOR
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 - ⊘ FUSIBLE RESISTOR
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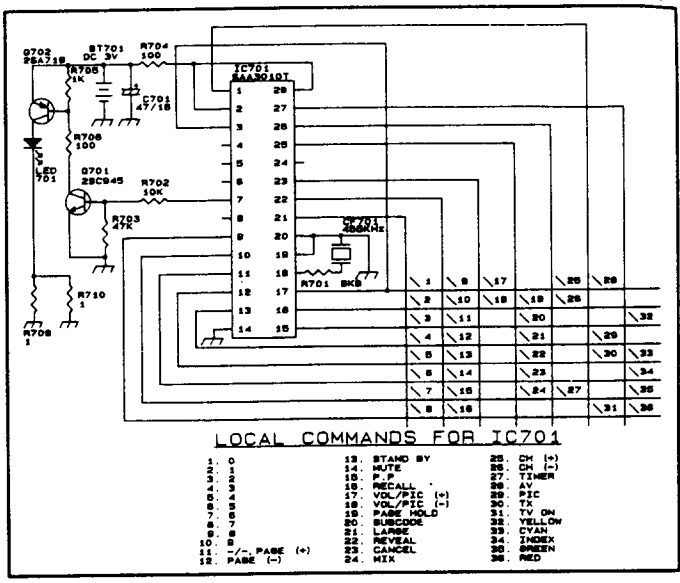
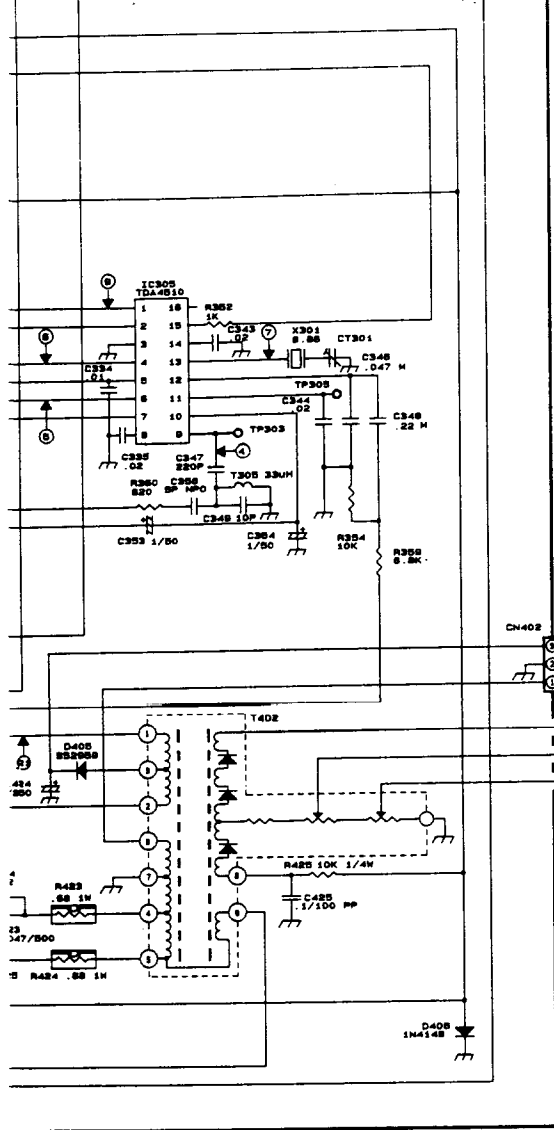




IC801 9AAS248				IC801 PCA84C40P				IC102 TDA4805				
VDD0	1	48	WE	VTUNN	1	42	VDD	ABC TAKE OVER	1	28	PHASE 2 DEP.	X7
OBCOUT	2	47	DE	VDL	2	41	STDBY	VERT. RAMP GENT.	2	27	SANDCASTLE OUTPUT/ FLYBACK INPUT	SBH
OBCIN	3	48	A12	BRI	3	40	SDA	VERT. DRIVE	3	26	HOR. DRIVE	Z0
OCSBO	4	45	A11	SAT	4	39	SCL	VERT. FEEDBACK	4	25	SYNC SEPARATION	Z1
VBSA	5	44	A10	CON/TON/HUE	5	38	SYSTEM	TUNER ABC	5	24	PHASE 1 DET.	Z2
REF+	6	43	A9	BAL/TON/HUE	6	37	EFFECT	BND	6	23	HOR. DEC.	Z3
BLACK	7	42	A8	VHF-L	7	36	SND0	SUPPLY	7	22	COINCIDENCE DET. DECOUPLING	HD DATA
CVBS	8	41	A7	VHF-H	8	35	RMOT	VIF INPUT	8	21	SYNC DEMODULATOR	DA7
INEF	9	40	A6	AFC	9	34	SND1	VIF INPUT	9	20	SYNC DEMODULATOR	DR7
VDDA	10	39	A5	UNF	10	33	RESET	DECOUPLING CAPACITOR	10	19	ABC DETECTION	DR6
POL	11	38	A4	VTR	11	32	XTAL2	VOL. CONTROL/ START HOR. DEC.	11	18	AFC OUTPUT	DR5
STTV/LES	12	37	A3	AY	12	31	XTAL1	AUDIO OUTPUT	12	17	VIDEO OUTPUT	DR4
VCR/PPS	13	36	A2	KEYB0	13	30	TEST	ROUND DEMODULATOR	13	16	BND	DR3
VBS0	14	35	A1	KEYB1	14	29	IDENT	SIF DECOUPLING	14	15	SIF INPUT	V85
R	15	34	A0	KEYB2	15	28	DOB0					
G	16	33	D7	KEYB3	16	27	VSYNC					
B	17	32	D6	KEYB4	17	26	HVSYNC					
NR0REF	18	31	D5	KEYB5	18	25	PBL					
BLAN	19	30	D4	KEYB6	19	24	BLUE					
CDR	20	29	D3	MOSTR	20	23	GREEN					
ODD/EVEN	21	28	D2	VBS	21	22	RED					
Y	22	27	D1									
SCL	23	26	DO									
SDA	24	25	VBS0									







NOTE:

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- (2) ALL RESISTORS ARE 50WV UNLESS OTHERWISE NOTED
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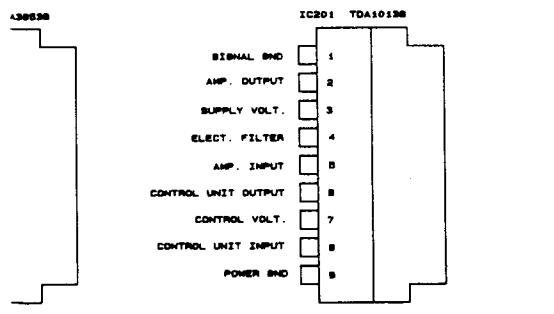
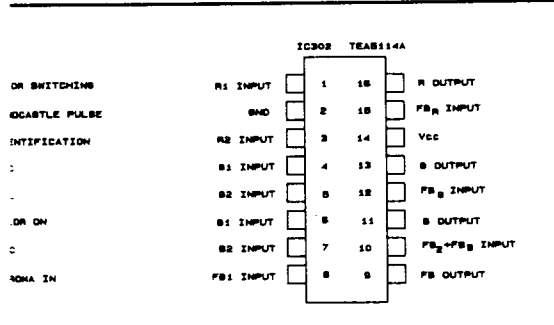
ELECTROLYTIC CAPACITOR
 BI-POLAR ELECTROLYTIC CAPACITOR
 TANTALUM CAPACITOR
 MYLAR CAPACITOR
 METALLIZED POLYESTER
 POLYESTER FILM CAPACITOR
 POLYPROPYLENE CAPACITOR
 METAL OXIDE RESISTOR
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GREAT WALL FRANCE
SCHEMATIC DIAGRAM N° 10

PAL 6G FTZ WITH SCART CONNECTOR AND 5 TEXT	SUITABLE FOR MODELS 8920 B - 9220 8821 - 8921 - 9213
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